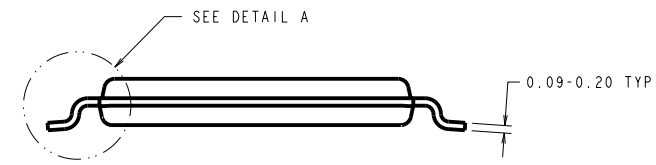
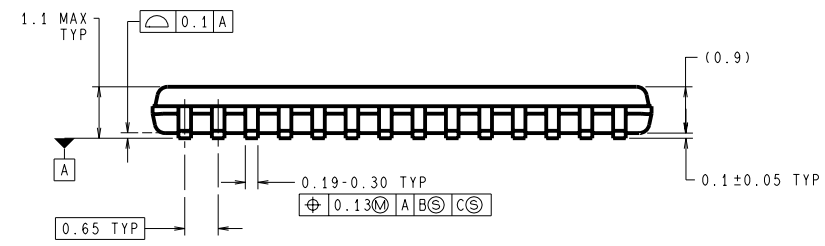
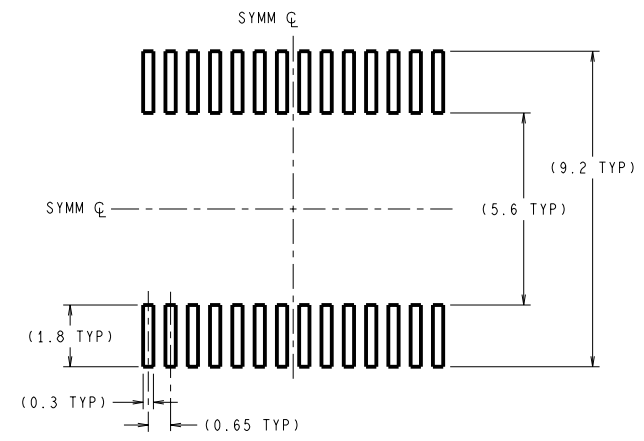
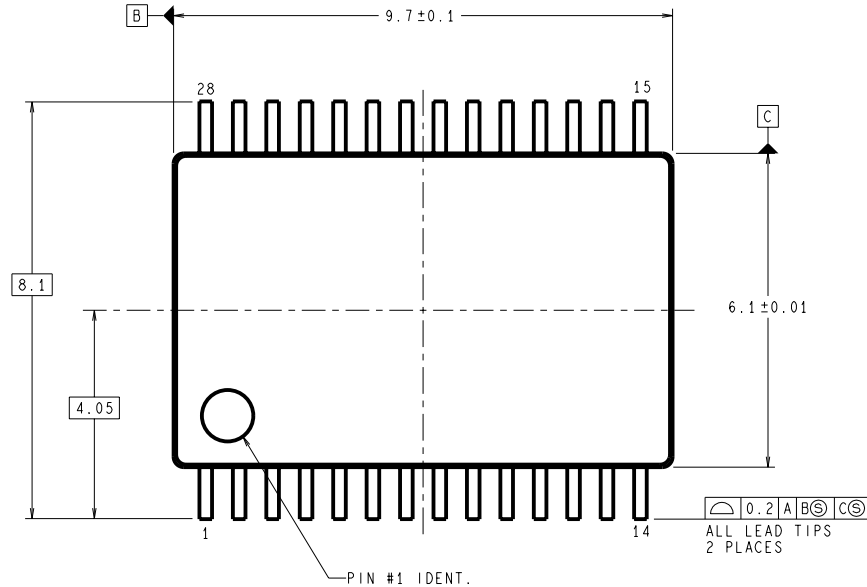
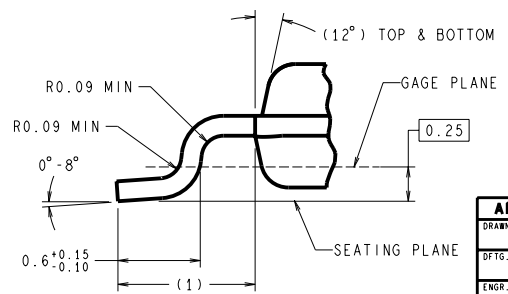


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	11379	03/07/96	MS/



DIMENSIONS ARE IN MILLIMETERS



NOTES: UNLESS OTHERWISE SPECIFIED  
 1. REFERENCE JEDEC REGISTRATION MO-153, VARIATION DB, DATED FEB, 1995.

APPROVALS		DATE	National Semiconductor		
DRAWN	MARTA SUCHY	03/07/96	2900 Semiconductor dr., Santa Clara, CA 95052-8090		
DFTG. CHK.			MOLDED TSSOP, JEDEC, 6.1mm BODY WIDTH, 28 LD, 0.65mm PITCH		
ENGR. CHK.					
PROJECTION		SCALE	SIZE	DRAWING NUMBER	REV
		N/A	C	MKT-MTE28	A
DO NOT SCALE DRAWING			SHEET 1 of 1		